

Serial No. 09/717,495

Docket: 680-016us



**IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE**

Inventor: Michael A. Fisher
Docket: 680-016us
Serial No.: 09/717,495
Filing Date: 20 November 2000
Examiner: Pranav Chandrasekhar
Group Art Unit: 2185

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Name of person signing this certificate: Lawrence Gabuzda

Signature 

Title: Sequencer and method of selectively inhibiting clock signals to execute reduced instruction sequences in a re-programmable I/O interface

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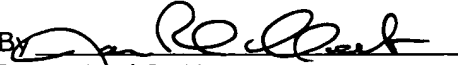
Technology Center 2100

Enclosed are the following papers related to the above-identified patent application:

1. Transmittal Letter with Certificate of Mailing - 1 Page (1x)
2. Amendment with Certificate of Mailing - 6 Pages (1x)
3. Return Postcard.

Pursuant to 37 CFR 1.136(a)(3), please treat this and any concurrent or future reply in the application that requires a petition for extension of time for its timely submission as incorporating a petition for extension of time for the appropriate length of time.

Respectfully,
DeMont & Breyer, LLC

By 
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Date 1/5/04

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AMENDMENT

The following claims are presented for examination:

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